

1/3

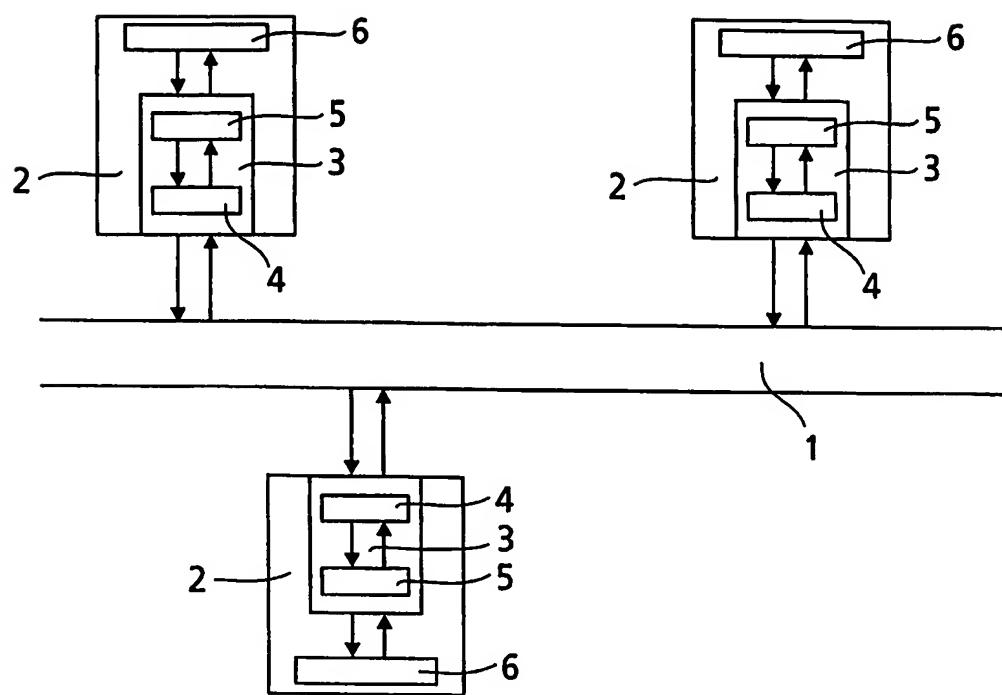


Fig. 1

2/3

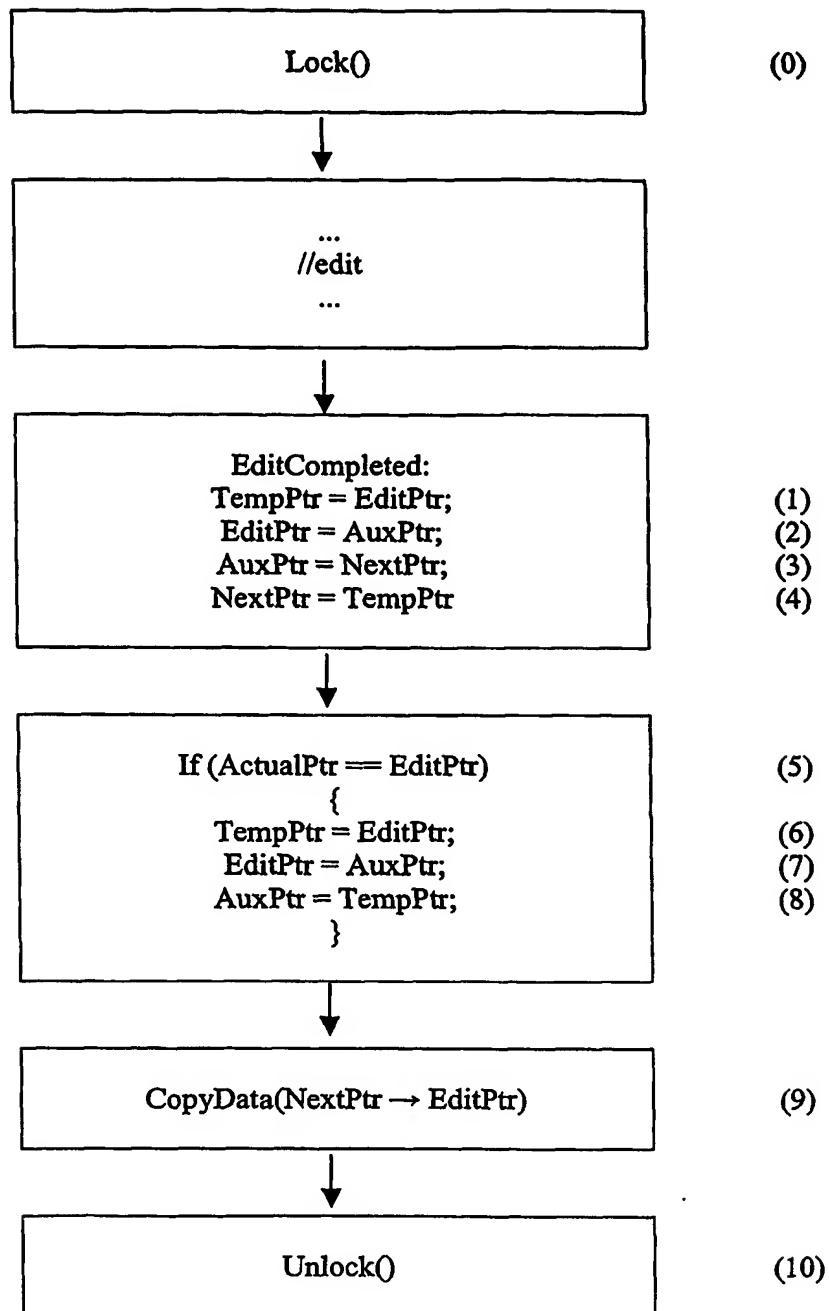


Fig. 2

3/3

T	ActualPtr	NextPtr	EditPtr	AuxPtr	Description
T0	A(0)	A(0)	B(0)	C(?)	Initial stage
T1	A(0)	A(0)	B(0')	C(?)	Edit
T2	A(0)	A(0)	B(1)	C(?)	(0), Editing ended
T3	A(0)	B(1)	C(?)	A(0)	(1) ... (4), test (5) -> "FALSE"
T4	A(0)	B(1)	C(1)	A(0)	(9)
T5	B(1)	B(1)	C(1)	A(0)	Reset operation
...					
T10	B(1)	B(1)	C(1')	A(0)	Edit
T11	B(1)	B(1)	C(2)	A(0)	(0), Editing ended
T12	B(1)	C(2)	A(0)	B(1)	(1) ... (4), test (5) -> "FALSE"
T13	B(1)	C(2)	A(2)	B(1)	(9)
T14	B(1)	C(2)	A(2')	B(1)	Edit
T15	B(1)	C(2)	A(3)	B(1)	(0), Editing ended
T16	B(1)	A(3)	B(1)	C(2)	(1) ... (4), test(5) -> "TRUE"
T17	B(1)	A(3)	C(2)	B(1)	(6) ... (8)
T18	B(1)	A(3)	C(3)	B(1)	(9)
T19	A(3)	A(3)	C(3)	B(1)	Reset operation
...					
T30	A(1)	B(2)	C(3)	A(1)	(0), Editing ended
T31	B(2)	B(2)	C(3)	A(1)	Reset operation
T32	B(2)	C(3)	A(1)	B(2)	(4), test (5) -> FALSE
T33	B(2)	C(3)	A(3)	B(2)	(9)
...					
T40	A(1)	B(2)	C(3)	A(1)	(0), Editing ended
T41	A(1)	C(3)	A(1)	B(2)	(4)
T42	C(3)	C(3)	A(1)	B(2)	Reset operation, test (5) -> "FALSE"
T43	C(3)	C(3)	B(3)	A(1)	(9)
...					
T50	A(1)	B(2)	C(3)	A(1)	(0), Editing ended
T51	A(1)	C(3)	A(1)	B(2)	(4), test (5) -> "TRUE"
T52	C(3)	C(3)	A(1)	B(2)	Reset operation
T53	C(3)	C(3)	B(2)	A(1)	(6) ... (8)
T54	C(3)	C(3)	B(3)	A(1)	(9)
...					

Fig. 3